UTILITY UTILITY SPATENT APPLICATION TRANSMITTAL (Saily for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No. 249344US2S DIV

First Inventor or Application Identifier Yukihiro URAKAWA

Title MEMORY CHIP AND SEMICONDUCTOR DEVICE USING THE MEMORY CHIP AND MANUFACTURING METHOD OF THOSE

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019	See	APPLICATION ELEMENTS MPEP chapter 600 concerning utility patent application contents	Commissioner for Patents ADDRESS TO: Mail Stop Patent Application Alexandria, Virginia 22313	
1.		Fee Transmittal Form (e.g. PTO/SB/17) (Submit an original and a duplicate for fee processing)	ACCOMPANYING APPLICATION PARTS	
			7. Assignment Recorded at Reel/Frame: 013088/0895	
2.		Specification Total Sheets 35	8. Application Data Sheet. See 37 CFR 1.76	
			9. 37 C.F.R. §3.73(b) Statement Power of Attorney	
3.		Drawing(s) (35 U.S.C. 113) Total Sheets 16	10. ☐ English Translation Document (if applicable)	
			11. Information Disclosure Copies of IDS Statement (IDS)/PTO-1449 Citations	
4.		Oath or Declaration Total Pages 2	12. Preliminary Amendment	
	a.	☐ Newly executed (original or copy)	13. White Advance Serial No. Postcard	
	b.	Copy from a prior application (37 C.F.R. §1.63(d)) (for continuation/divisional with box 17 completed)	14. Certified Copy of Priority Document(s) (if foreign priority is claimed)	
		 i. DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b). 	15. Applicant claims small entity status. See 37 CFR 1.27	
5.		CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)	16. Other: Request for Priority	
6.		Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)		
	a.	Computer Readable Form (CRF)		
	b.	Specification or Sequence Listing on :		
		i. ☐ CD-ROM or CD-R (2 copies); or ii. ☐ Paper		
,	c.	☐ Statements verifying identity of above copies		
47				
17.		CONTINUING APPLICATION, check appropriate box, and supply Continuation Divisional Continuation-		
f		application information: Examiner: TRAN, M. H.	Group Art Unit: 2818	
For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only when a portion has been inadvertently omitted from the submitted application parts.				
18. CORRESPONDENCE ADDRESS				
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	Katherine D. Pauley	Registration No.:	50,607



249344US2S DIV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Yukihiro URAKAWA

SERIAL NO: New DIV Application

FILING DATE: Herewith

FOR:

MEMORY CHIP AND SEMICONDUCTOR DEVICE USING THE MEMORY CHIP AND

MANUFACTURING METHOD OF THOSE

FEE TRANSMITTAL

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	17 - 20 =	0	x \$18 =	\$0.00
INDEPENDENT CLAIMS	2 - 3 =	0	x \$86 =	\$0.00
☐ MULTIPLE DEPENDEN	T CLAIMS (If app	licable)	+ \$290 =	\$0.00
☐ LATE FILING OF DECL	ARATION		+ \$130 =	\$0.00
BASIC FEE			\$770.00	
TOTAL OF ABOVE CALCULATIONS			\$770.00	
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Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Date: 2704

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